

Receipt date: 06/27/2011

PTO/SB/08a (01-08)

Approved for use through 05/31/2008. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number: 10/755,042 Filing Date: January 9, 2004 First Named Inventor: Mou-Shiung Lin Art Unit: 2815 Examiner Name: Jerome Jackson Jr.	
Sheet	1	of	1
Attorney Docket No: 085027-0104			

US PATENT DOCUMENTS					
Examiner Initial *	Cite No	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NONE			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ²
		NONE				

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	"Electronic Materials Handbook, Volume 1 – Packaging: Other Design Considerations; Materials and Electronic Phenomena; Physical Characteristics of Microelectronic Materials" ASM International Handbook Committee, pgs. 104-111, ASM International (1989)	
	2	KUO, "Semiconductor Packaging Engineering", Zhan Yi-Zheng Publisher, Registration Taipei, April 2000, Table 9.1	

EXAMINER

/Jerome Jackson Jr/ (08/19/2011)

DATE CONSIDERED

DM_US 27832804-1.085027.0104

0540TW, 0537TW NPL

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /JJ/